

FIG. 1A (PRIOR ART)

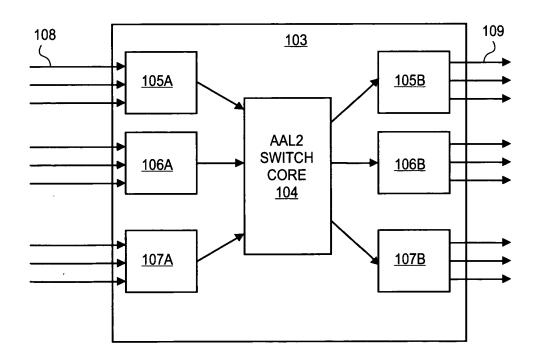
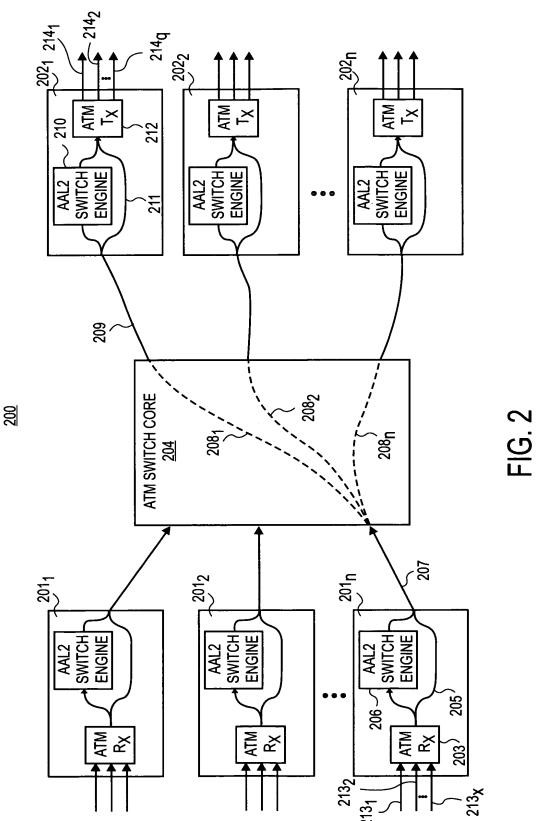


FIG. 1B (PRIOR ART)



IN			OUT	
PORT#	VPI/VCI	CID	EID	P#
A ₁	A ₂	A ₃	A ₄	- A ₅
B ₁	В2	В3	В4	B ₅
C ₁	C ₂	B ₃ C ₃	C ₄	C ₅
		•	, , ,	
91	q ₂	q ₃	94	95

<u>300</u>

FIG. 3

IN		OUT				
EID	CID	VPI/VCI	PORT#			
X ₄	X ₆	X ₇	X ₈			
X ₄ Y ₄	X ₆ Y ₆	Y ₇	Y ₈			
Z ₄	Z ₆	Z ₇	Z ₈			
• • •						
m ₄	m ₆	m ₇	m ₈			

<u>400</u>

FIG. 4

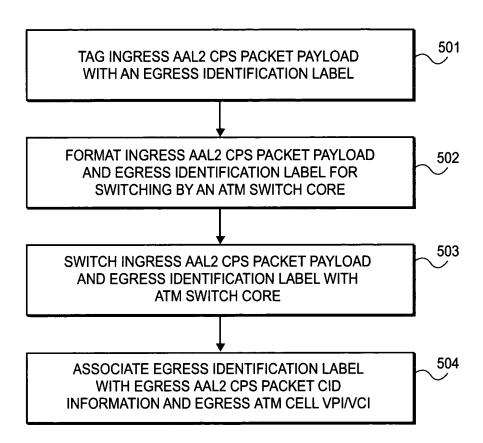


FIG. 5